Title: Ultra-low power inverter
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In this framework, the aim of this Special Issue is to attract reviews and original research outcomes related to the design and application of ultra-low-voltage/power, ...

This paper presents a novel 1-bit 20T-HyDGFA architecture, boosting the synergistic benefits of DG MOSFETs with domino logic to meet the crucial requirements of ...

This paper introduces an inverter-based OTA designed in a 65 nm CMOS technology, showcasing exceptionally low power consumption and an extremely low supply ...

This single inverter gate is designed for 1.65V to 5.5V VCC operation. The SN74LVC1G04 device performs the Boolean function Y = A. The CMOS device has high output drive while ...

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