Three-phase inverter with hysteresis control

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This note shows how to implement an FPGA-based hysteresis controller with automated VHDL code generation with HDL Coder, and validation through simulation.

Abstract -- This paper presents a new hysteresis current regulation strategy for the neutral point clamped (NPC) and flying capacitor (FC) three-level inverters.

Therefore, this paper describes the control of a three-phase grid-connected inverter system for generating electricity at the distribution end. The control method ...

To improve the power quality of grid connected inverter, different methods of hysteresis current controller are studied under dynamic conditions.

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